

LESSON PLAN

Period	Date (Tentative)	Topic	Unit No	Teaching Methodology	Remarks	Corrective Action Upon Review
1	26-12-13	8086/8088 Microprocessor	I	CD		
2	27-12-13	Register Organization of 8086, Architecture, description of 8086, Physical memory organization, general bus operation, I/O addressing capability, Special purpose registers, maximum mode, minimum mode of 8086 system and timing, the processor 8088, Machine language instructions format, addressing modes of 8086		u		
3	28-12-13	Instruction set of 8086, assembler directives and operations.		u		
4	29-12-13	Programming with 8086	II	u		
5	30-12-13	8086 Microprocessor Machine level programs		u		
6	31-12-13	programming with an assembler, assembly language programs, introduction to stack, stack structure of 8086/8088, interrupts and interrupt service routines, interrupt cycle of 8086		u		
7	01-01-14	non maskable interrupt and maskable interrupts		u		
8	02-01-14	interfacing interrupt. programming		u		
9	03-01-14	8086 Microprocessor	III	u		
10	04-01-14	their interfacing with 8086/88		u		
11	05-01-14	Semiconductor memory interfacing, dynamic		u		
12	06-01-14					
13	07-01-14					
14	08-01-14					
15	09-01-14					
16	10-01-14					
17	11-01-14					

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18	21-01-18	RAM interfacing, inter		CR		
19	22-01-18	facings I/O ports, PIOBS				
20	23-01-18	modes of operation of		4		
21	24-01-18	8255, interfacing to				
22	25-01-18	D/A and A/D converters		4		
23	26-01-18	stepper motor interfa-				
24	27-01-18	cing, control of high		4		
25	28-01-18	power devices using				
		8255.		4		
26	29-01-18	Special purpose Programmable peripheral	19	CR		
27	30-01-18	devices and their				
		interfacing!		4		
28	02-02-18	programmable interrupt				
		controller 8259A, the				
29	05-02-18	key board / display		4		
30	06-02-18	controller 8279, program-				
		mable communication		4		
31	07-02-18	interface 8251 USART,				
		DMA controller 8257,		4		
32	08-02-18	programmable with DMA				
		interface 8237		4		
33	09-02-18	Advanced microprocessor	20	CR		
		80386:				
		Salient features of		4		
34	19-02-18	80386, architecture				
		and signal description		4		
35	20-02-18	of 80386, register des-				
		cription of 80386		4		
36	21-02-18	and addressing modes				
		data types of 80386,		4		
37	22-02-18	real address mode of				
		80386, protected mode		4		
		of 80386, segmentation				
		and paging, virtual		4		
38	24-02-18	80386 mode and en-				
		hanced mode, inst.		4		

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Period	Date (Tentative)	Topic	Unit No.	Teaching Methodology	Remarks	Corrective Action Upon Review
		struction Set of 80386		CR		
39	25-02-14	the co-processor 80387, the CPU with a number		y		
40	26-02-14	the co-processor - 80486 DX.		y		
41	28-02-14	8051 microcontroller	XI	y		
42	01-03-14	introduction to micro controllers, 8051		y		
43	03-03-14	micro controllers, 8051 pin description,		y		
44	05-03-14	connections, I/O ports and memory organization,		y		
45	06-03-14	8051 addressing modes and instructions, assembly		y		
46	07-03-14	language programming		y		
47	08-03-14	tools.				
48	10-03-14					
49	12-03-14					
50	18-03-14	PIC Microcontroller	XII	CR		
51	20-03-14	overview and features PIC 14CXX/7X instructions,		y		
52	21-03-14	interrupts in PIC 16C61/71, PIC		y		
53	24-03-14	16F8XX flash controller, data EEPROM, and flash EEPROM,		y		
54	26-03-14	E/O ports and timers		y		
55	27-03-14					
56	7-04-13	ARM 32-Bit Microcontroller: Introduction to 16/32 Bit	XIII	CR		
57	9-04-13	processors, ARM architecture		y		
58	10-04-13	features and organization, ARM/Thumb programming model,		y		
59	11-04-13					
60	12-04-13	ARM/Thumb instruction set, development tools.		y		